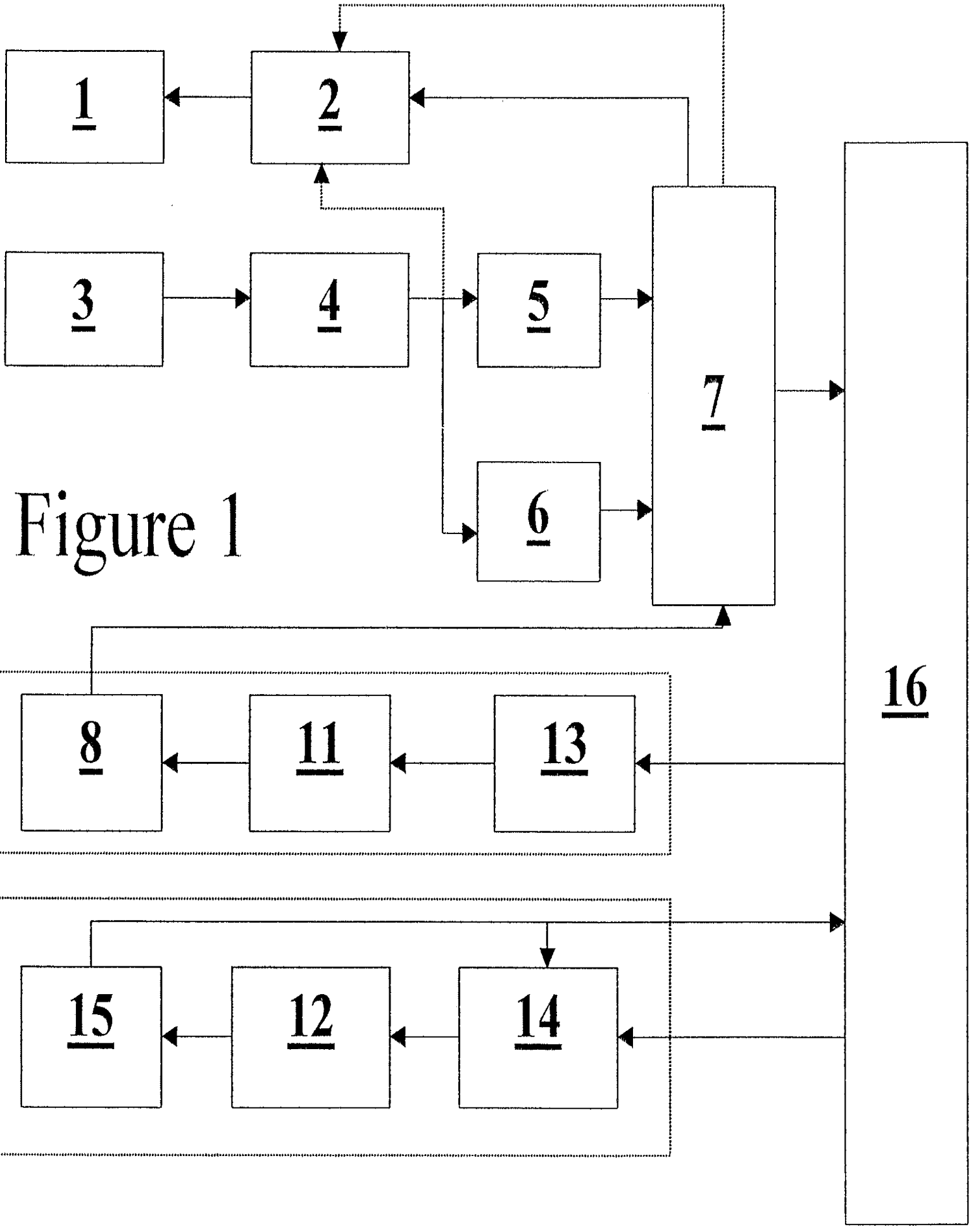


Figure 1

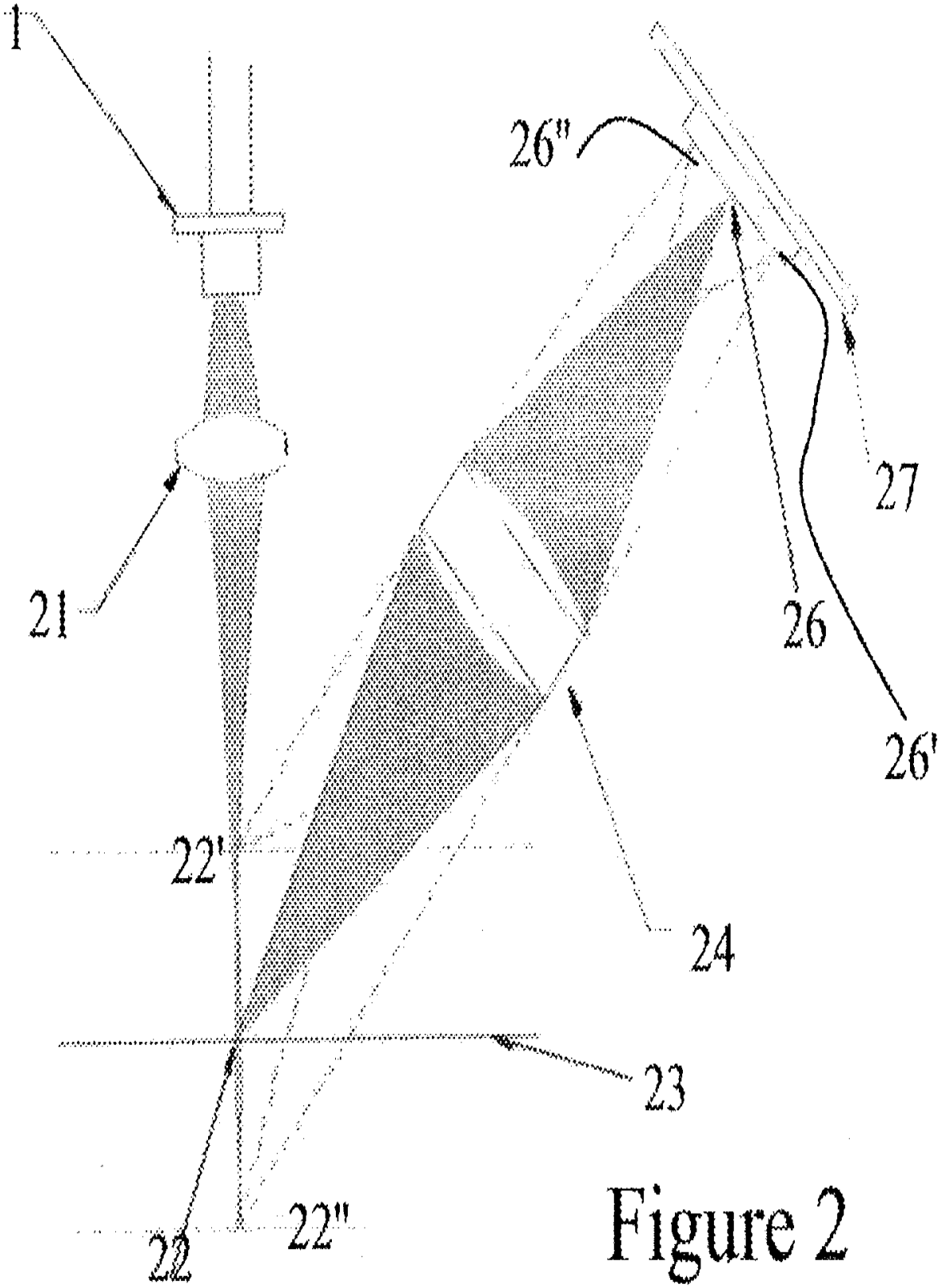


Figure 2

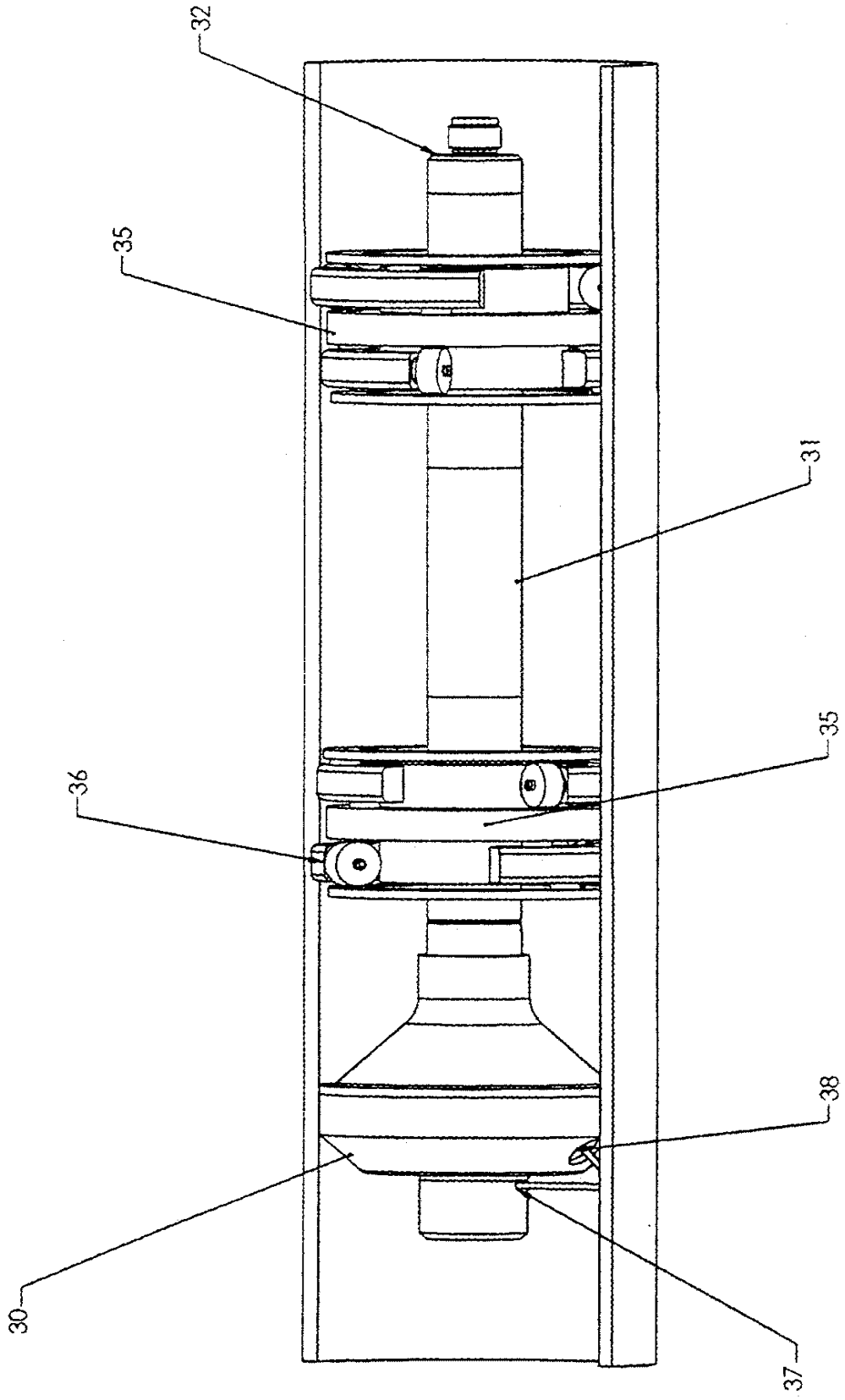
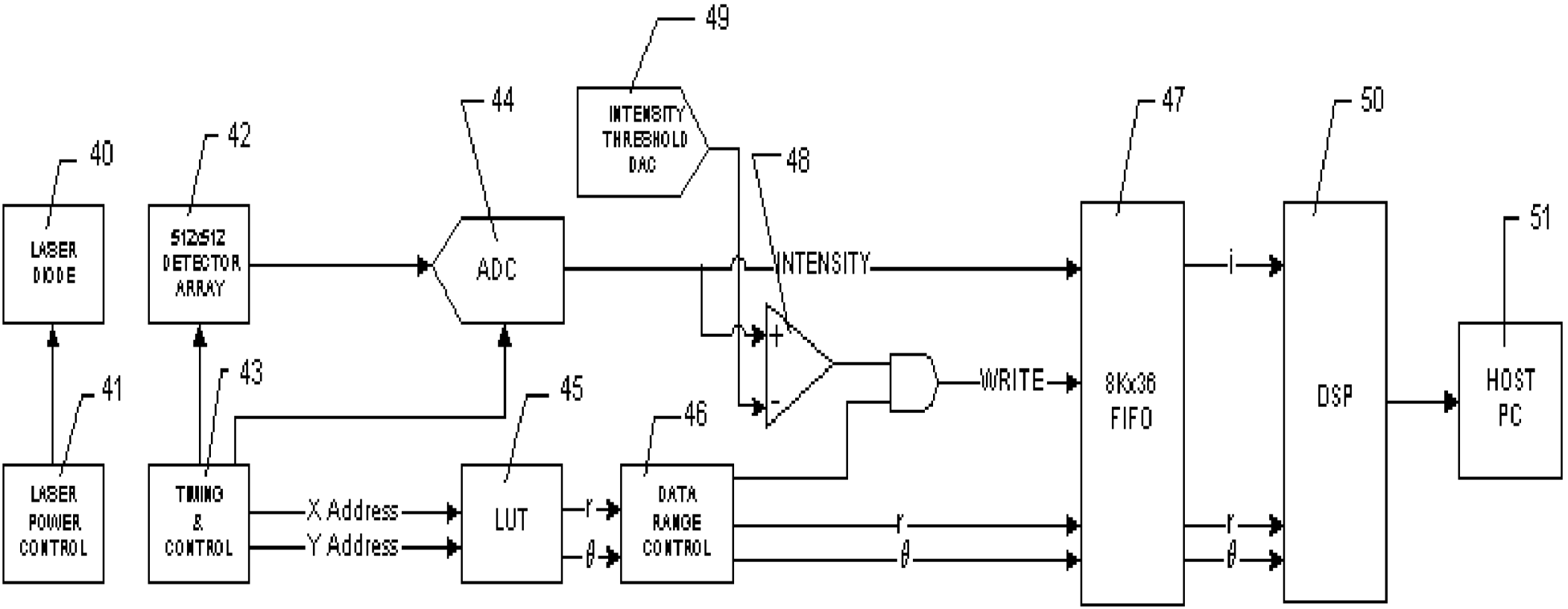


Figure 3.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ADC
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51
40
LASER DIODE
42
512x512 DETECTOR ARRAY
INTENSITY
8Kx36 FIFO
DSP
HOST PC
41
LASER POWER CONTROL
43
TIMING & CONTROL
X Address
Y Address
45
LUT
r
θ
46
DATA RANGE CONTROL
WRITE
i

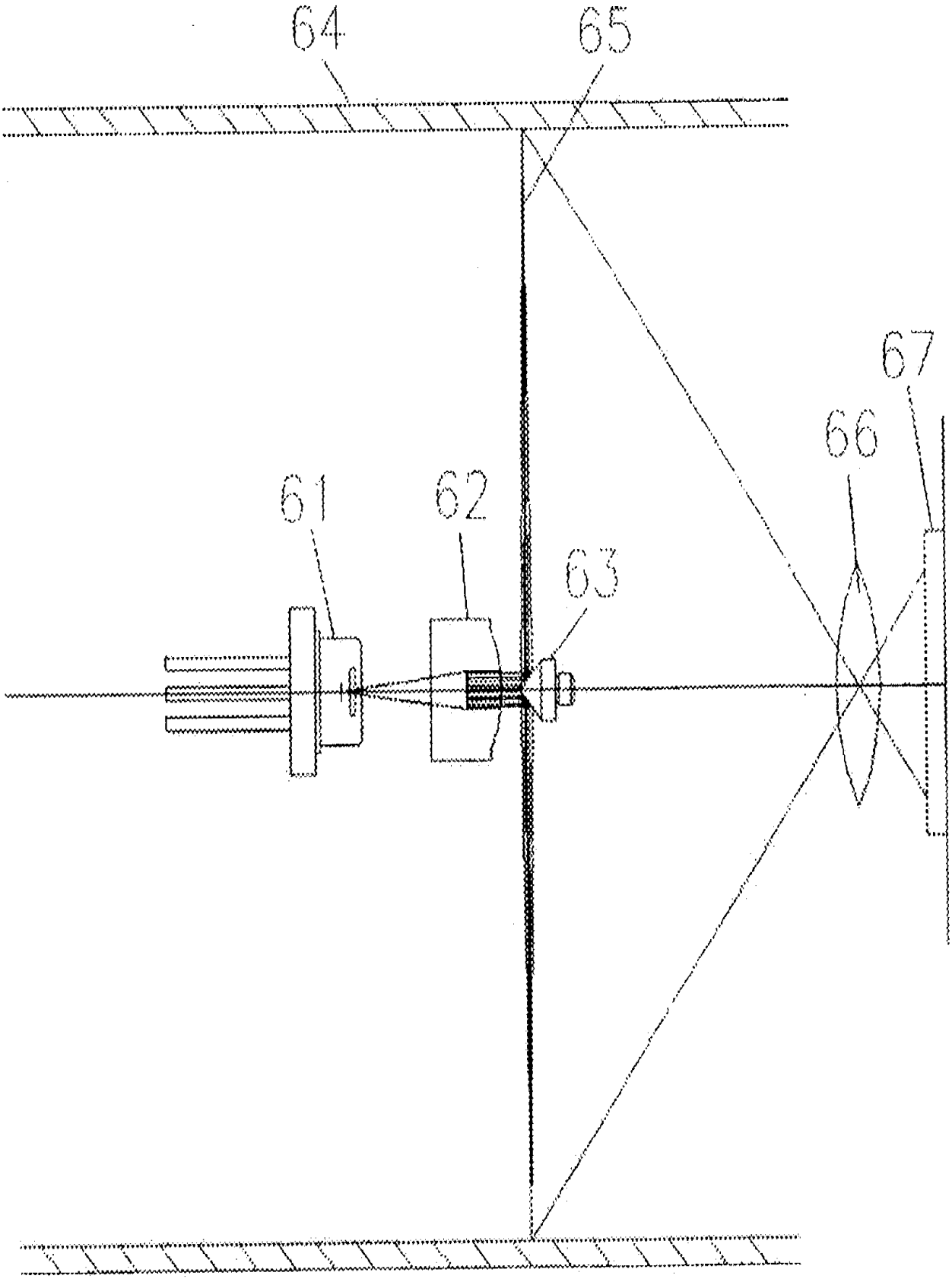

Figure 5